

Group Art Unit: 2812

Confirmation No.: 6537



PAGE 1/3 ° RCVD AT 1/21/2005 3:43:20 PM [Eastern Standard Time] * SVR:USP10-EFXRF-2/5 * DNIS:7469195 * CSID:202 955 3751 * DURATION (mm-5s):01-22

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Practitioner's Docket No.: SON-2626

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of: Koichi Takeuchi

Serial No.:

10/506,792

Filing Date:

September 7, 2004

A METHOD OF PRODUCING A SEMICONDUCTOR DEVICE AND

SEMICONDUCTOR DEVICE

Attention: Office of Initial Patent Examination's

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REQUEST FOR CORRECTED FILING RECEIPT

- 1. Attached is a copy of the official filing receipt received from the PTO in the above application for which issuance of a corrected filing receipt is respectfully requested.
- 2. There is an error with respect to the following, which is incorrectly entered.

Error in

Correct data

1. Title

1. A METHOD FOR PRODUCING A SEMICONDUCTOR DEVICE AND SEMICONDUCTOR DEVICE

3. The correction is due to an error by the USPTO. If any fee is due, please charge Deposit Account No.

Dated: January 21, 2005

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FILING OR 371 APPL NO. ART UNIT FIL FEE REC'D (c) DATE ATTY.DOCKET NO DRAWINGS TOT CLMS IND CLMS 10/506,792 09/07/2004 2812 920 SON-2626 14 20 3

23353 RADER FISHMAN & GRAUER PLLC LION BUILDING 1233 20TH STREET N.W., SUITE 501 WASHINGTON, DC 20036 CONFIRMATION NO. 6537 FILING RECEIPT *OC000000014874865*

Date Mailed: 01/05/2005

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt Incorporating the requested corrections (if

Applicant(s)

Koichi Takeuchi, Kanagawa, JAPAN;

Power of Attorney:

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Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP03/03454 03/20/2003

Foreign Applications

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Non-Publication Request: No

Early Publication Request: No

Title

202 955 3751 P.02

Page 2 of 2

A Method
Process for producing semiconductor device and semiconductor device

Preliminary Class

438

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